

01-17-02 2800 2811 #4/8302

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Sean T. Crowley, William M. Anderson, Bradley D. Boland, Eric Bergman  
Assignee: Amkor Technology, Inc.  
Title: Power Semiconductor Package With Strap  
Serial No.: 10/008,048 Filing Date: November 7, 2001  
Examiner: Unknown Group Art Unit: Unassigned  
Docket No.: M-11561 US

San Francisco, California  
January 14, 2002

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the Applicants call to the attention of the Examiner the patent applications listed below. Copies of these documents are enclosed.

1. U.S. Patent Application Serial Number 09/733,148, filed 12/7/00, and the art cited therein;
2. U.S. Patent Application Serial Number 09/587,136, filed 6/2/00, and the art cited therein;
3. U.S. Patent Application Serial Number 09/536,236, filed 3/27/00, and the art cited therein.

A PTO Form 1449 listing these documents is attached hereto.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

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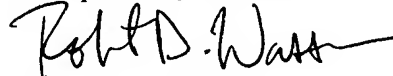
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

EXPRESS MAIL LABEL NO:

EL 751 263 172 US

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.		Serial No.	
						M-11561 US		10/008,048	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)						Applicants			
						Crowley, Sean T.; Anderson, William M.; Boland, Bradley D.; Bergman, Eelco			
						Filing Date		Group	
						November 7, 2001			
U.S. Patent Documents									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA								
	AB								
	AC								
	AD								
	AE								
	AF								
	AG								
	AH								
	AI								
	AJ								
	AK								
Foreign Patent Documents									
							Translation		
		Document	Date	Country	Class	Subclass	Yes	No	
	AL								
	AM								
	AN								
	AO								
	AP								
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
	AQ	Glenn et al., "Exposed Copper Strap In A Semiconductor Package," U.S. Patent Application No. 09/733,148, filed December 7, 2000, pp. 1-15 and 5 sheets of figures.							
	AR	Crowley et al., "Packaging High Power Integrated Circuit Devices," U.S. Patent Application No. 09/587,136, filed June 2, 2000, pp. 1-26 and 4 sheets of figures.							
	AS	Crowley et al., "Attaching Semiconductor Dies To Substrates With Conductive Straps," U.S. Patent Application No. 09/536,236, filed March 27, 2000, pp. 1-22 and 4 sheets of figures.							
Examiner			Date Considered						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.									